

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(324/76.82.ccls.) and @ad<"20031008" and @pd>"20050711"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/02/01 10:05
L2	8	(324/76.77,76.11.ccls.) and @ad<"20031008" and @pd>"20050711"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/02/01 10:11
L4	87	(324/763,765.ccls.) and @ad<"20031008" and @pd>"20050711"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/02/01 10:44
L5	6	(324/76.16,73.1.ccls.) and @ad<"20031008" and @pd>"20050711"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/02/01 11:24
L6	51	(324/158.1.ccls.) and @ad<"20031008" and @pd>"20050711"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/02/01 11:25
L7	81	(714/724,726.ccls.) and @ad<"20031008" and @pd>"20050711"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/02/01 11:29
L8	8	(702/127.ccls.) and @ad<"20031008" and @pd>"20050711"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/02/01 12:28

L11	1	((counter and (operati\$5 adj2 parameter) and (voltage near2 (change variation difference modification adjustment alternation deviation)) and (state mode status) and (IC (integrated adj circuit) \$6chip ASIC) and digital\$3).clm.)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/02/01 12:46
L12	2	((counter and (operati\$5 adj2 parameter) and (voltage near2 (change variation difference modification adjustment alternation deviation)) and (state mode status) and (IC (integrated adj circuit) \$6chip ASIC).clm.)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/02/01 14:33
L15	3	((counter and (operati\$5 adj2 parameter) and (voltage near2 (change variation difference modification adjustment alternation deviation)) and (check\$3 detect\$3 sens\$3 measur\$5 comput\$3 calculat\$3 \$2valuat\$3 examin\$5 test\$3 determin\$3 recogniz\$3 inspect\$3 anal\$5 monitor\$3 diagnos\$3 identit\$7 record\$5 meter\$5) and (IC (integrated adj circuit) \$6chip ASIC).clm.)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/02/01 12:44
L16	314	((counter and (operati\$5 adj2 parameter) and (voltage near3 (change variation difference modification adjustment alternation deviation)) near3 (check\$3 detect\$3 sens\$3 measur\$5 comput\$3 calculat\$3 \$2valuat\$3 examin\$5 test\$3 determin\$3 recogniz\$3 inspect\$3 anal\$5 monitor\$3 diagnos\$3 identit\$7 record\$5 meter\$5) and (state mode status) and (IC (integrated adj circuit) \$6chip ASIC) and digital\$3)) and @ad<"20031008"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/02/01 12:52
L17	8	116 and ((counter same (state mode status) same (IC (integrated adj circuit) \$6chip ASIC) same digital\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/02/01 14:31
L18	425	((voltage near3 (change variation difference modification adjustment alternation deviation)) near3 (check\$3 detect\$3 sens\$3 measur\$5 comput\$3 calculat\$3 \$2valuat\$3 examin\$5 test\$3 determin\$3 recogniz\$3 inspect\$3 anal\$5 monitor\$3 diagnos\$3 identit\$7 record\$5 meter\$5) and (mode near2 (IC (integrated adj circuit) \$6chip ASIC)) and @ad<"20031008"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/02/01 12:53
L19	12	118 and ((counter same (state mode status) same (IC (integrated adj circuit) \$6chip ASIC) same digital\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/02/01 14:45

L20	4	118 and (operati\$ adj2 parameter) near3 (IC (integrated adj circuit) \$6chip ASIC)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/02/01 14:36
L22	16	118 and (counter and (state mode status) and (operati\$ adj2 parameter) and (IC (integrated adj circuit) \$6chip ASIC) and digital\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/02/01 14:49